

In the Claims:

1. (Previously Presented) A constant current bias circuit associated with a linear power amplifier comprising:

an at least one resistor;

a capacitor coupled to the at least one resistor;

a bias voltage input terminal for receipt of a bias voltage connected to the at least one resistor; and

an at least one transistor connected to the at least one resistor by an electrical path resulting in a constant bias current when the bias voltage is present.

2. (Previously presented) The constant current bias circuit of claim 1, further including:

a clamp circuit coupled by an electrical path to the at least one resistor that provides a minimum bias current.

3. (Previously Presented) The constant current bias circuit of claim 1, wherein the constant bias current is in a linear relationship with the bias voltage.

4. (Previously Presented) The constant current bias circuit of claim 1, further including:

a circuit having a plurality of components electrically coupled to the at least one resistor in receipt of the constant bias current.

5. (Previously Presented) The constant current bias circuit of claim 4, wherein the at least one resistor is in a first material in a substrate and at least one component of the plurality of components is in a second material in the substrate and different from the first material.

6. (Original) The constant current bias circuit of claim 5, wherein the first material is

Complementary Metal Oxide Semiconductor fabrication material.

7. (Original) The constant current bias circuit of claim 6, wherein the second material is Gallium Arsenide Semiconductor fabrication material.

8. (Original) The constant current bias circuit of claim 4, wherein the circuit is a single stage amplifier.

9. (Original) The constant current bias circuit of claim 4, wherein the circuit is a multi-stage amplifier.

10. (Previously Presented) The constant current bias circuit of claim 1, further including a feedback loop that maintains a quiescent bias for a reference transistor included in the feedback loop equal to a reference current, wherein the reference current is mirrored from the constant bias current.

11. (Previously Presented) A constant current bias circuit associated with a linear power amplifier comprising:

an at least one resistor;

a capacitor coupled to the at least one resistor;

means for receiving a bias voltage connected to the at least one resistor; and

an at least one transistor connected to the at least one resistor by an electrical path resulting in a constant bias current when the bias voltage is present.

12. (Previously presented) The constant current bias circuit of claim 11, further including means for providing a minimum bias current coupled by an electrical path to the at least one resistor.

13. (Previously Presented) The constant current bias circuit of claim 11, wherein the constant bias current is in a linear relationship with the bias voltage.

14. (Previously Presented) The constant current bias circuit of claim 11, further including a circuit having a plurality of components electrically coupled to the at least one resistor in receipt of the current bias current.

15. (Previously presented) The constant current bias circuit of claim 14, wherein the at least one resistor is in a first material in a substrate and at least one component of the plurality of components is in a second material in the substrate and different from the first material.

16. (Original) The constant current bias circuit of claim 15, wherein the first material is Complementary Metal Oxide Semiconductor fabrication material.

17. (Original) The constant current bias circuit of claim 16, wherein the second material is Gallium Arsenide Semiconductor fabrication material.

18. (Original) The constant current bias circuit of claim 14, wherein the circuit is a single stage amplifier.

19. (Original) The constant current bias circuit of claim 14, wherein the circuit is a multi-stage amplifier.

20. (Previously Presented) The constant current bias circuit of claim 11, further including a means for generating a feedback loop to maintain a quiescent bias for a reference transistor included in the feedback loop equal to a reference current, wherein the reference current is mirrored from the current bias current.

21. (Previously Presented) A method for constant current biasing associated with a linear power amplifier, the method comprising:

receiving an input bias voltage;

determining if the input bias voltage is below a predetermined threshold; and

generating a bias current I_{bias} by at least one resistor connected by an electrical path to at least one transistor being in receipt of the input bias voltage.

22. (Canceled) The method of claim 21, further including the step of determining if the input bias voltage is below a predetermined threshold.

23. (Previously presented) The method of claim 22, further including the step of activating a clamp circuit to assure the bias current I_{bias} is above a predetermined threshold.

24. (Previously presented) The method of claim 21, further including the step of mirroring the bias current I_{bias} to a reference current I_{ref} by a predetermined ratio.

25. (Previously presented) The method of claim 24, further including the step of receiving the reference current I_{ref} at a transistor in a second material different from a first material, wherein the bias current was generated in the first material.

26. (Previously presented) The method of claim 25, wherein the first material is CMOS and shares a substrate with the second material.

27. (Previously presented) The method of claim 21, further including the step of maintaining a feedback loop that provides a quiescent bias for a reference transistor included in the feedback loop equal to a reference current I_{ref} , wherein the reference I_{ref} is mirrored from the bias current bias.